

## Refine Search

### Search Results -

Term	Documents
MULTIPLEXER	72178
MULTIPLEXERS	25014
(76 AND MULTIPLEXER).USPT.	2
(L76 AND MULTIPLEXER ) USPT.	2

**Database:**

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

**Search:**

L77

### Search History

DATE: Monday, May 16, 2005 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

DB=USPT; PLUR=YES; OP=ADJ

<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
			result set
<u>L77</u>	L76 and multiplexer	2	<u>L77</u>
<u>L76</u>	L75 and allocation	3	<u>L76</u>
<u>L75</u>	L74 and decoding	7	<u>L75</u>
<u>L74</u>	L73 and receiver and frequency	27	<u>L74</u>
<u>L73</u>	frequency adj block and broadband adj signal	28	<u>L73</u>
<u>L72</u>	frequency adj block and plurality adj broadband	0	<u>L72</u>
<u>L71</u>	L70 and tuning	0	<u>L71</u>
<u>L70</u>	L69 and backup	3	<u>L70</u>
<u>L69</u>	L67 and inputs	7	<u>L69</u>
<u>L68</u>	L67 and plurality adj inputs	0	<u>L68</u>
<u>L67</u>	frequency adj block and backbone and receivers and broadband	9	<u>L67</u>

<u>L66</u>	frequency adj block and common adj backbone	0	<u>L66</u>
<u>L65</u>	L63 and backbone	0	<u>L65</u>
<u>L64</u>	L63 and bus	0	<u>L64</u>
<u>L63</u>	L62 and frequency	1	<u>L63</u>
<u>L62</u>	L61 and multiplexer	2	<u>L62</u>
<u>L61</u>	plurality adj broadband adj signals and inputs	6	<u>L61</u>
<u>L60</u>	L59 and modem	1	<u>L60</u>
<u>L59</u>	L57 and cable adj TV	2	<u>L59</u>
<u>L58</u>	L57 and cable adj television adj modem	0	<u>L58</u>
<u>L57</u>	broadband adj signal adj inputs	17	<u>L57</u>
<u>L56</u>	broadband adj signal adj inputs and cable adj modem	0	<u>L56</u>
<u>L55</u>	L54 and frequency adj allocation	2	<u>L55</u>
<u>L54</u>	L53 and frequency	6	<u>L54</u>
<u>L53</u>	L52 and multiplexer	6	<u>L53</u>
<u>L52</u>	L51 and allocation	8	<u>L52</u>
<u>L51</u>	L50 and frequency adj block	11	<u>L51</u>
<u>L50</u>	L49 and bus	904	<u>L50</u>
<u>L49</u>	broadband and signal adj inputs	3928	<u>L49</u>
<u>L48</u>	L46 and tune	0	<u>L48</u>
<u>L47</u>	L46 and tuning	0	<u>L47</u>
<u>L46</u>	L45 and frequency and multiplexer	8	<u>L46</u>
<u>L45</u>	back-up adj receiver and broadband	9	<u>L45</u>
<u>L44</u>	back-up adj receiver and broadband adj input	0	<u>L44</u>
<u>L43</u>	backup adj receiver and broadband adj input	0	<u>L43</u>
<u>L42</u>	L40 and backup adj receiver	0	<u>L42</u>
<u>L41</u>	L39 and back-up adj receiver	0	<u>L41</u>
<u>L40</u>	L39 and tuning	4	<u>L40</u>
<u>L39</u>	L37 and frequency and block	5	<u>L39</u>
<u>L38</u>	L37 and frequency adj block	0	<u>L38</u>
<u>L37</u>	L36 and receiver	5	<u>L37</u>
<u>L36</u>	L35 and allocation	5	<u>L36</u>
<u>L35</u>	L34 and multiplexer	12	<u>L35</u>
<u>L34</u>	L33 and bus	15	<u>L34</u>
<u>L33</u>	broadband adj signals and assigned adj frequency	57	<u>L33</u>
<u>L32</u>	L31 and multiplexer	1	<u>L32</u>
<u>L31</u>	L29 and decoding	1	<u>L31</u>
<u>L30</u>	L29 and decode	0	<u>L30</u>
<u>L29</u>	L27 and frequency and block	1	<u>L29</u>
<u>L28</u>	L27 and frequency adj block	0	<u>L28</u>
<u>L27</u>	L26 and tuning	1	<u>L27</u>
<u>L26</u>	backup adj receiver and broadband	3	<u>L26</u>

<u>L25</u>	back-up adj receiver and broadband	9	<u>L25</u>
<u>L24</u>	backup adj receiver and broadband adj input	0	<u>L24</u>
<u>L23</u>	L19 and allocate adj frequency	0	<u>L23</u>
<u>L22</u>	L19 and frequency adj allocation	0	<u>L22</u>
<u>L21</u>	L19 and plurality adj receiver	0	<u>L21</u>
<u>L20</u>	L19 and multiple adj receivers	0	<u>L20</u>
<u>L19</u>	L17 and tuning	11	<u>L19</u>
<u>L18</u>	L17 and tuning adj frequency	0	<u>L18</u>
<u>L17</u>	L15 and decoding	12	<u>L17</u>
<u>L16</u>	L15 and frequency adj block	0	<u>L16</u>
<u>L15</u>	L14 and receivers	13	<u>L15</u>
<u>L14</u>	L13 and allocation and frequency	15	<u>L14</u>
<u>L13</u>	bus and multiplexer and broadband adj inputs	30	<u>L13</u>
<u>L12</u>	L11 and bus	0	<u>L12</u>
<u>L11</u>	L10 and decoding	3	<u>L11</u>
<u>L10</u>	L9 and receivers	3	<u>L10</u>
<u>L9</u>	L8 and allocation	3	<u>L9</u>
<u>L8</u>	L7 and multiplexer	3	<u>L8</u>
<u>L7</u>	L6 and broadband adj inputs	4	<u>L7</u>
<u>L6</u>	370/535.ccls.	667	<u>L6</u>
<u>L5</u>	370.535.ccls.	0	<u>L5</u>
<u>L4</u>	L3 and block	1	<u>L4</u>
<u>L3</u>	L2 and assigned adj frequency	1	<u>L3</u>
<u>L2</u>	L1 and allocation and broadband	6	<u>L2</u>
<u>L1</u>	multiplex adj signals and plurality adj inputs	236	<u>L1</u>

END OF SEARCH HISTORY